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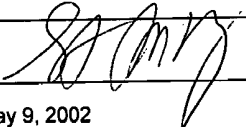
PTO/SB/21 (08-00)

Approved for use through 10/31/2002. OMB 0651-0031

U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>	Application Number	09/854,905
	Filing Date	May 15, 2001
	First Named Inventor	MOORE, Brian
	Group Art Unit	2829
	Examiner Name	
Total Number of Pages in This Submission		Attorney Docket Number 11157-23

ENCLOSURES (check all that apply)		
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Date	May 9, 2002

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Stephen M. Beney B.Sc. (Physics)
416 957 1697 sbeney@bereskinparr.com

Your Reference: 09/854,905
Our Reference: 11157-23

**SUPPLEMENTAL INFORMATION
DISCLOSURE STATEMENT**

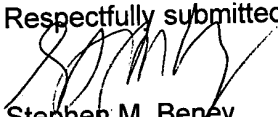
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Dear Sir:

Re: Patent Application No. 09/854,905
For: WIRELESS RADIO FREQUENCY TECHNIQUE DESIGN AND METHOD
FOR TESTING OF INTEGRATED CIRCUITS AND WAFERS
Applicant: MOORE, Brian
Filed: May 15, 2001
Group: 2829

In accordance with 37 CFR 1.56 and 1.97, applicant hereby submits a Supplemental Information Disclosure Statement and provides an English translation of a foreign document (WO 99/17353) that was provided in an earlier Information Disclosure Statement. Form PTO-1449 is also enclosed. Please have these documents recorded against the above-mentioned application.

Respectfully submitted,


Stephen M. Beney
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